

ATAL Academy

ATAL Programme is an initiative by AICTE which aims at empowering faculty to achieve goals of Higher Education such as access, equity and quality. These programs have been designed to fulfill the need to train the young generation in skill sector and having faculty & technicians to be trained in their respective disciplines. The objectives of ATAL FDP are

- To support technical institutions in fostering research innovation and entrepreneurship through training.
- To stress upon empowering technical teachers & technicians using ICT
- To provide a variety of opportunities for training and exchange of experiences such as workshops, orientations, learning communities, peer mentoring and other FDPs.

About the Institute

K L University is a pioneer in the field of engineering education and research in private sector. It was established as Koneru Lakshmaiah College of Engineering in the year 1980. The institute is accredited by National Assessment and Accreditation Council (NAAC) of UGC with A grade K L University has knowledge base stems with the presence of eminent and highly qualified academic staff, numbering 600+ out of which 155+ doctorates and 300+ pursuing Ph.D. is one of the best for any engineering institute. KLU offers 10 UG programs, 14 PG programs, 28 Integrated degree programs, dual degree programs and in addition to this research program is been offered in 14 disciplines.

About the Department

Department of Electronics & Communication Engineering was established in the year 1980, has grown now to be a department with 110 Faculty members and over 2000 Undergraduate and 200 Post Graduate students and about 200 research scholars. As the largest academic department of University, Department of ECE and its Faculty are involved in an exceptional range of Educational, Intellectual and Research activities. We have grown over the years in many directions and currently recognized as a "Premier Teaching and Research Department" of the KL University and also, we recognized by DST under FIST program in the year 2012. Being as FIST-DST recognized department, recently we purchased the 18 GHz Network Analyzer, 20 GHz Spectrum Analyzer and HFSS Suite etc., In addition, the department is well equipped with various laboratories catering the UG and PG Programs and other research centers in VLSI, Communication Systems, Atmospheric Sciences and MEMS.

About the FDP

The course aims at faculty members/research scholars of engineering institutions and polytechnic approved by AICTE, officials from industry and other Research and Development organizations. The program will also inspire the faculty members and research scholars to generate new ideas in their research area.

Objectives of FDP

The primary goal of the FDP is to promote research and developmental activities to solve problems related to Advanced VLSI (Chip) Logic Design, Physical Design in current IoT era. This creates an opportunity to equip with the ongoing Technological development in the area of VLSI and IoT. The program is intended to provide a vibrant opportunity for Electronics, Electrical, Instrumental & Mechanical students and research scholars.

Topics to be Covered

- VLSI Implications of the Internet of Things.
- The Challenges and Opportunities of Nanoscale CMOS Technology: Architecture and Modeling to Optimize 5G and IoT Applications.
- Design and Aging Challenges in FinFET Circuits and Internet of Things (IoT) Applications
- FinFET Technology for Energy-Constrained IoT Applications
- State-of-the-Art Understanding of Circuit Aging for IoT
- Potential Design Solutions for Addressing IoT Circuit Aging
- IoT Lifetime: Battery vs. Chip Lifetime
- Exploring Transistor Aging in IoT Applications
- Promising Candidates of Embedded, Low-Power Memory for IoT

Outcome of FDP

Participants can able to:
Solve practical and state of the art IoT problems to serve VLSI & MEMS industries.

AICTE ATAL Sponsored

Five Days Faculty Development Program

On

Challenges and Opportunities in

VLSI for IoT Applications

June 21st – 25th 2021



(DEEMED TO BE UNIVERSITY)

Organized by
Microelectronics Research Group
Department of ECE

Chairperson

Dr.M.Suman

HOD, Department of ECE

Coordinator

Dr. K. Srinivasa Rao

Professor, Department of ECE

Co-Coordinators

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Dr.K.Girija Sravani, Asst. Professor, Dept. of ECE

Mr.M.Aditya, Asst. Professor, Dept. of ECE

CHIEF PATRON

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PATRONS

Er K Raja Harin, Vice-President, KLEF

Er K L Havish Vice-President, KLEF

Dr.S S Mantha, Chancellor, KLEF

Dr.N.Venkatram, Pro-Vice Chancellor, KLEF

Dr.Y.VSSV. Prasad Rao, Registrar, KLEF

Dr. K.Subba Rao, Principal, KLUCE

Dr.B.J.K Singh, Dean (R&D), KLEF

University Advisory Committee

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Dr.GV Subba Rao, Professor & RPAC, KLEF

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Dr. A.Aravindhan

Dr. Sourabh Upadhyay

Dr. N P Singh

Dr. Atul Kumar

Dr.GRK Prasad

& other members of Microelectronics Research Group.

Resource Persons

Renowned experts and researchers from premier Institutions (IIT/NITs) and other organizations (State/central) and industries will deliver expert lectures.

The FDP is open to faculty members, research scholars, officials from industry and other Research and Development organizations. The Soft copy of study material, training PPT's will be shared with all the participants.

Registration Fee

No registration fee

Requirements to get E-Certificate

1. Minimum **80% attendance** is required for the whole course
2. **Minimum 60% marks** should be obtained in the

Final test to be conducted online at the end of FDP.

Contact details of Coordinator

Dr.K.Srinivasa Rao, Professor

Department of ECE

Koneru Lakshmaiah Education Foundation
(Deemed to be University)

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